Γ

1/4 10 15 > 20 -45 55 AUTHENTICATION ENGINE DMA CONTROLLER 80 -*30* > 50 CONTROLLER CLOCK CONTROL — CLK 25 85 -⟨35 < 40 MEMORY REFERENCE FILE TIMER MODULE

FIG. 1

 Γ

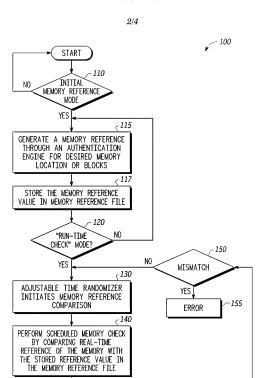


FIG. 2

 Γ



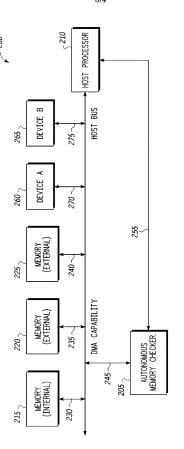


FIG 3

